

Electronic Acknowledgement Receipt

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Application Number:	10811835
International Application Number:	
Confirmation Number:	3051
Title of Invention:	Method of designing a circuit layout of a semiconductor device
First Named Inventor/Applicant Name:	Kazuaki Goto
Customer Number:	78198
Filer:	Donald R. Studebaker
Filer Authorized By:	
Attorney Docket Number:	030712-29
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Submitted with Payment	yes
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Appeal Brief Filed	appealbrief3-30-09.pdf	805632 885601498578d307d99b74d2155ccc24020 56608	no	12
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	31858 9018720469e34c2150e4f54de2e3668bdf2 cfbe1	no	2
Warnings:					
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Total Files Size (in bytes):				837490	
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